



26  
5-3003  
RW

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 09/661,912

Filing Date: September 14, 2000

Applicant: Saeed AZIMI

Group Art Unit: 2181

Examiner: Justin King

Title: HIGH LATENCY INTERFACE BETWEEN HARDWARE  
COMPONENTS

Attorney Docket: MP0043

**RECEIVED**

**MAY 29 2003**

**Technology Center 2100**

---

**DRAWING CORRECTION APPROVAL REQUEST**

Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

May 27, 2003

Dear Sir:

Applicants respectfully request the Examiner's approval of the following drawing corrections indicated in red ink on the attached sheet. The following are the corrections.

In Fig. 1, please add the label --PRIOR ART--.

In Fig. 3, please remove the header.

In Fig. 4, please remove the header.

No new matter has been entered via the drawing corrections. Corrected formal drawings will be prepared in accordance with the Notice of Draftperson's Patent Drawing Review Form PTO-948 and will be filed upon approval by the Examiner and subsequent indication of allowance of the present application.

If there are any outstanding matters remaining in this application, the Examiner is invited to contact Donald J. Daley, Reg. No. 34,313, at the number listed below in the Washington, D.C. area in order to discuss these matters.

If necessary, the Commissioner is hereby authorized in this, concurrent, and further replies, to charge payment or credit any overpayment to Deposit Account No. **08-0750** for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

HARNESS, DICKEY & PIERCE, P.L.C

By:

  
Donald J. Daley, Reg. No. 34,313

DJD:ewd

P.O. Box 8910  
Reston, Virginia 20195  
(703) 668-8000

Attachment: Three (3) sheets of red-inked drawings